



Ms. April Moon  
 Digital Electronics (DE) 2024-2025

Email: [april.moon@redoakisd.org](mailto:april.moon@redoakisd.org)



<b>4 TALONS OF THE HAWK</b>			
<b>ACADEMICALLY PREPARED</b>	<b>OPEN TO CHALLENGES OF LEARNING</b>	<b>FAIR, RESPECTFUL, &amp; WELL ROUNDED</b>	<b>LEAVE A LEGACY</b>
<ul style="list-style-type: none"> <li>• 1% better every day</li> <li>• Love &amp; Accountability</li> </ul>	<ul style="list-style-type: none"> <li>• GRIT- Growth, Resilience, Integrity, Tenacity</li> </ul>	<ul style="list-style-type: none"> <li>• REACH- Respect, Encourage, Appreciate, Communicate, Honor</li> </ul>	<ul style="list-style-type: none"> <li>• We Before Me (Service)</li> </ul>

**Ms. Moon's Schedule:**

1 <sup>st</sup> Period 8:20 - 9:06	2 <sup>nd</sup> Period 9:12 - 9:58	3 <sup>rd</sup> Period 10:04 – 10:50	4 <sup>th</sup> Period 10:56 - 11:42	5 <sup>th</sup> Period 11:46 - 1:42 *includes lunch	6 <sup>th</sup> Period 1:48 - 2:35	7 <sup>th</sup> Period 2:41 - 3:28	8 <sup>th</sup> Period 3:34 - 4:21
PLTW District Coordinator	Engineering Design and Development	Engineering Science	Engineering Design and Development	Conference	Aerospace Engineering	Digital Electronics	Engineering Science

**Tutoring Hours: 8:00-8:15 AM and after-school tutoring as scheduled**

**Digital Electronics (DE) Course Description**

Digital electronics is the study of electronic circuits that are used to process and control digital signals. In contrast to analog electronics, where information is represented by a continuously varying voltage, digital signals are represented by two discrete voltages or logic levels. This distinction allows for greater signal speed and storage capabilities and has revolutionized the world of electronics. The major focus of the DE course is to expose students to the design process of combinational and sequential logic design, teamwork, communication methods, engineering standards, and technical documentation.



**Digital Electronics Outline**

Unit 1: Foundations in Electronics	Unit 2: Combinational Logic	Unit 3: Sequential Logic	Unit 4: Controlling Real-World Systems
Lesson 1.1 Intro to Electronics  Lesson 1.2 Intro to Circuit Design	Lesson 2.1 AOI Combinational Logic Circuit Design  Lesson 2.2 Alternative Design: Universal Gates and K-Mapping  Lesson 2.3 Specific Combinational Logic Designs  Lesson 2.4 Intro to Programmable Logic Devices	Lesson 3.1 Sequential Logic Circuit Design  Lesson 3.2 Asynchronous Counters  Lesson 3.3 Synchronous Counters	Lesson 4.1 Intro to State Machines  Lesson 4.2 Application of State Machines



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### **Class Culture**

The culture in my classroom is student-centered – where I facilitate lessons, but you individually drive your learning through exploration, creative and critical thinking, collaboration, and carrying out the steps of the engineering design process. Not only will we learn STEM concepts, but I hope you develop a stronger love for learning. We will also focus on developing ‘life’ skills, including skills related to teamwork, professional communication, and project management. My classes are rigorous, and my expectations are high, but the rewards are great!

It is important that you take thorough hand-written notes in your engineering journals since your notes will be your main source of information (not a textbook), and it is vital that you come to class prepared - with all required supplies and a focus on learning.

We will explore engineering through exciting activities and projects that will allow a deeper understanding of the concepts being learned. For major projects, a detailed design brief, grading rubric, and the project’s due date will be supplied upfront so that expectations are clear. All projects will incorporate criteria related to creativity, proper documentation, accurate computations, content connections, evidence of the student’s journey through the engineering design process (proof of concept), effective teamwork, proper project management, a final product/presentation, and metacognition.

### **Classroom Rules**

1. Safety is our first priority! Therefore, all lab rules must be strictly followed. Students must be signed off to use tools and equipment, and an engineering instructor must be present when tools are used. Nobody is allowed in the shop area or the storage room without Mrs. Moon’s or Mr. Labram’s permission.
2. Respect Property:
  - Use furniture properly.
  - Please dispose of trash whether it is yours or not.
  - Please ensure all supplies / tools are put up in their designated ‘home’ neatly.
  - Please do not disturb items around or in my desk, and my teacher laptop is strictly off limits.
3. Eye contact is important, both with me and your classmates. Therefore, for the duration of class, all hair must be kept away from your eyes.
4. Computers will be used for academic purposes during designated times only. Proper electronic etiquette will be followed when others are speaking, and no online games are ever allowed in my lab (except the ones I am using for instructional purposes)!
5. Please dispose of food, trays, and food packages in the trash can OUTSIDE my room. Eating in my room is a privilege, not a right.

*“Freedom and responsibility go hand in hand!”*

### **Classroom Procedures**

1. Everyone must participate. This is how our class will reach its full potential as a team.
2. Respect others even when it requires *intentional* effort. We will work as a team in my class.



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3. Units must be shown, and the steps used for all solutions must be neatly recorded.
  4. Your journal should be your first resource for questions. Your second resource is your team.
  5. The teacher dismisses class. Please do not line up at the door before dismissal.

### **Absences and Make-up Work**

\*See district handbook.

If possible, work missed due to absences should be picked up and attempted before returning to class so the student better connects with the new lessons. This is especially important for assignments related to a team project. NOTE: Per campus policy, if you are absent for school-related or pre-scheduled activities/events on the day an assignment is due, you are required to turn in the assignment prior to the absence for the event.

NOTE: A zero will be entered into the gradebook for any missed/late work (even if you are granted an extension due to an absence), but that grade will be updated when the make-up work is submitted.

### **Discipline**

\*See campus handbook.

Academic Dishonesty: Academic dishonesty includes cheating or copying the work of another student, unapproved use of technology including cell phones, plagiarism, and unauthorized communication between students during an examination. Consequences for academic dishonesty: Grade of zero, Referral, Student reflection assignment.

### **Grading Policy**

\*See campus handbook.

- 25% of 6 weeks grade: 6 or more Daily Grades
- 15% of 6 weeks grade: 3 or more Homework Grades
- 60% of 6 weeks grade: 2 or more Major (Test/Project) Grades

NOTE: Semester exam grades are 1/7<sup>th</sup> of the semester averages.

- ✚ Projects will be assessed using a rubric and will carry the weight of at least one test grade.
- ✚ Traditional tests and quizzes, peer evaluations, progress checks, live performance evaluations, and metacognition activities will factor into your grade.
- ✚ I may conduct unannounced evaluations of your engineering journals, and you will be periodically allowed to use your journals on assessments, so please ensure your journal is always up-to-date.

*Note: Units must be shown ALWAYS, and the process steps used to arrive at solutions must be neatly recorded. Oftentimes, in engineering, processes are graded at a heavier weight than the final answers!*

**\* This syllabus may change at the teacher's discretion. \***